

DATE	DATE	TITLE	
1/10/2008	JM	Cambridge	
CHK	CHK	PR	
YJ	YJ	PR	
APPROVAL	APPROVAL	1.0	
Bj	Bj	1.0	
TABLE CODE	TABLE CODE	August 24, 2008 14:34:27 PM	PAGE 2 OF 47

BOARD INFORMATION

SCHEMATIC ANNOTATIONS AND BOARD INFORMATION

PCI Devices

Devices	IDSEL#	REQ/INT#	Interrupts
Cardbus	AD25	3	A,B,C
USB	AD29(internal)	-	USB2.0 #0 (USB0) : A USB2.0 #1 (USB1) : D USB2.0 #2 (USB4) : C USB2.0 #3 (USB5) : E USB2.0 #4 (USB6) : F
Hub to PCI	AD30(internal)	-	B
LPC bridge/IDE/AC97/SMBUS	AD31(internal)	-	-
Internal MAC	AD24(internal)	-	E
AC Link	-	-	B
GLAN	-	-	F

Voltage Rails

VDC, COORE GPX, COORE P3.3V, COORE P3.3V, MICOM	Primary DC system power supply (7 to 21V) Core Voltage for CPU Core Voltage for GPU & CH3-M 3.3V always power rail (for Micom)
P1.8V	1.8V switched power rail (off in S3-S5)
P1.8V_AUX	1.8V switched power rail (off in S3-S5)
P0.8V	0.8V power rail for DDR (off in S3-S5)
P3.3V	3.3V switched power rail (off in S3-S5)
P3.3V_AUX	3.3V switched power rail (off in S4-S5)
P5.0V	5.0V switched power rail (off in S4-S5)
P5.0V_AUX	5.0V switched power rail (off in S4-S5)
P5.0V_A1W	5.0V always power rail

USB PORT Assign	PCI Express Assign
PORT #	PORT #
ASSIGNED TO	ASSIGNED TO
SYSTEM PORT 0	0 NC
SYSTEM PORT 1	1 Min Card 1 (WLAN)
SYSTEM PORT 2	2 NC
NC	3 LAN
NC	4 Min Card 2 (ROBSON or DVB-T)
Bluetooth	5 NC
Min PCI Express 2	NC
NC	NC
NC	NC

Crystal / Oscillator

TYPE	FREQUENCY	DEVICE	USAGE
Crystal	32.768KHz	ICH8-M MICOM	Real Time Clock
Crystal	10MHz	CLOCK-Generator	HID4F21692160 CK-305
Crystal	14.318MHz	LAN	Intel LAN
Crystal	25MHz		

LCD Pannel Detect (TBD)

Devices	Resolution	PANNE_L_DETECT_0

I C / SMB Address

Devices	Address	Hex	Bus
ICH8-M CPU Internal Sensor	Master 01x 1010 000x	7Ah	SMBUS Master
SODIMM0	1010 010x	A0h	Thermal Sensor
SODIMM1	0011 010x	A4h	-
Thermal Sensor on SODIMM0	0011 010x	30h	-
Thermal Sensor on SODIMM1	0011 010x	34h	-
CK-505M (Clock Generator)	1101 001x	D2h	Clock, Unused Clock Output Disable

REVISION HISTORY

See rev notes for more information.

REVISION	DATE	TITLE	DESIGNER	APPROVAL	DATE	TIME
0004-22C-015(1395; 5.5) REV. 3	1/10/2008	Cambr idge CHIPSET POWER UNDEF INED	JM	PR	August 24, 2008	14:34:27 PM
0004-22C-015(1395; 5.5) REV. 3	1/10/2008	Cambr idge CHIPSET POWER UNDEF INED	JM	PR	August 24, 2008	14:34:27 PM

SAMSUNG

ELECTRONICS

BA-41 - 009xXA

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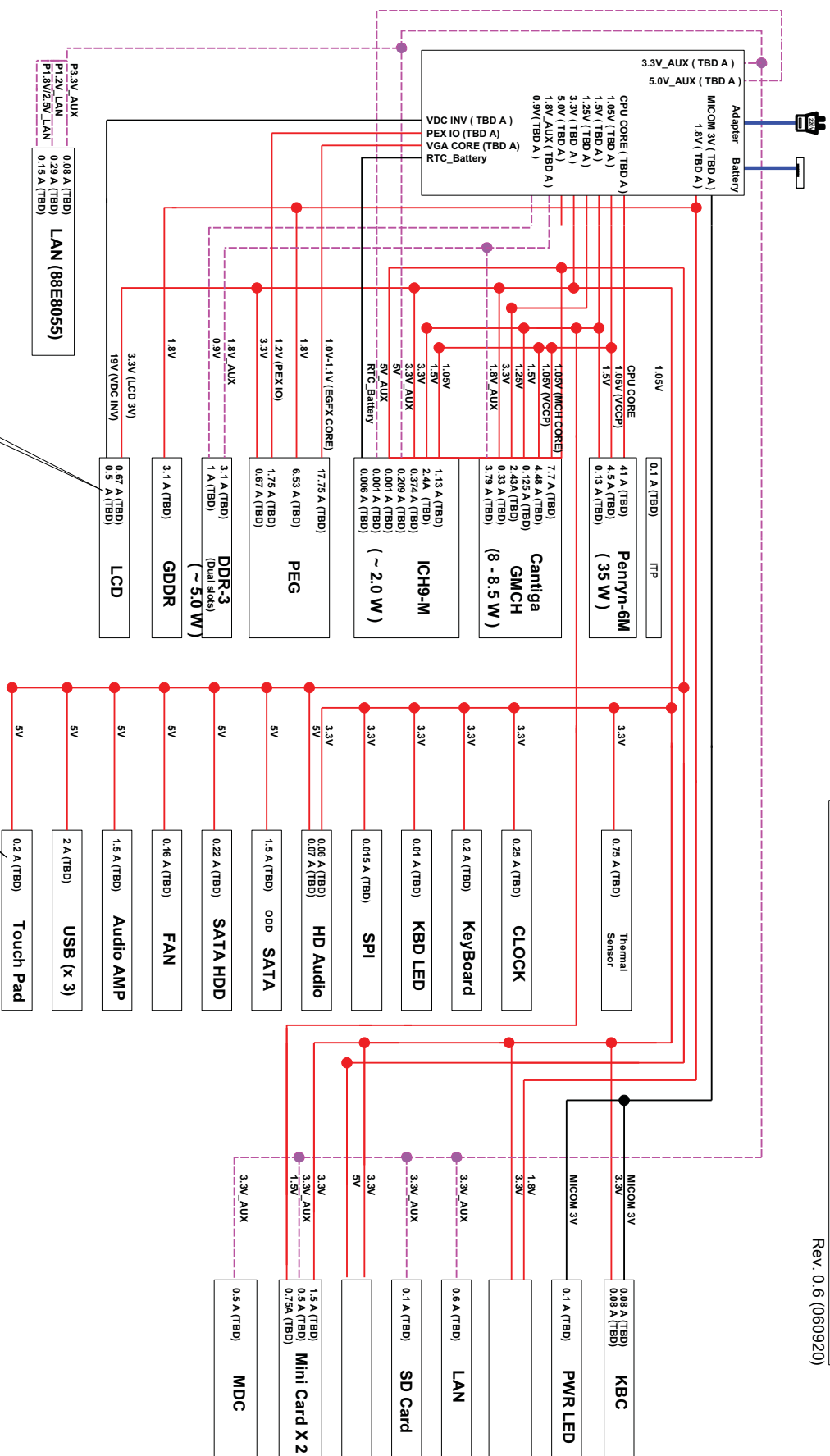
Rev 0.1



DATE	DATE	TITLE	SAMSUNG ELECTRONICS
CHECK	JM	1/10/2008	
BY	YJ	REV. STEP	
APPROVAL	REV	PR	
BU	1.0	POWER DIAGRAM	PART NO. BA-41 - 009xxA

POWER RAILS ANALYSIS

Rev. 0.6 (060920)



Value by Datasheet/Application notes (Value by measurement)

REVISION		DATE		TITLE	
DESIGN	JH	1/10/2008		Cambridge	
CHECK	YJ	REV		PR	
APPROVAL	BJ	REV		UNDEFINED	
TABLE CODE	LAST EDIT		August 24, 2008 14:34:27 PM		

SAMSUNG		ELECTRONICS	
CHIPSET POWER		BA41-009XA	

POWER SEQUENCE

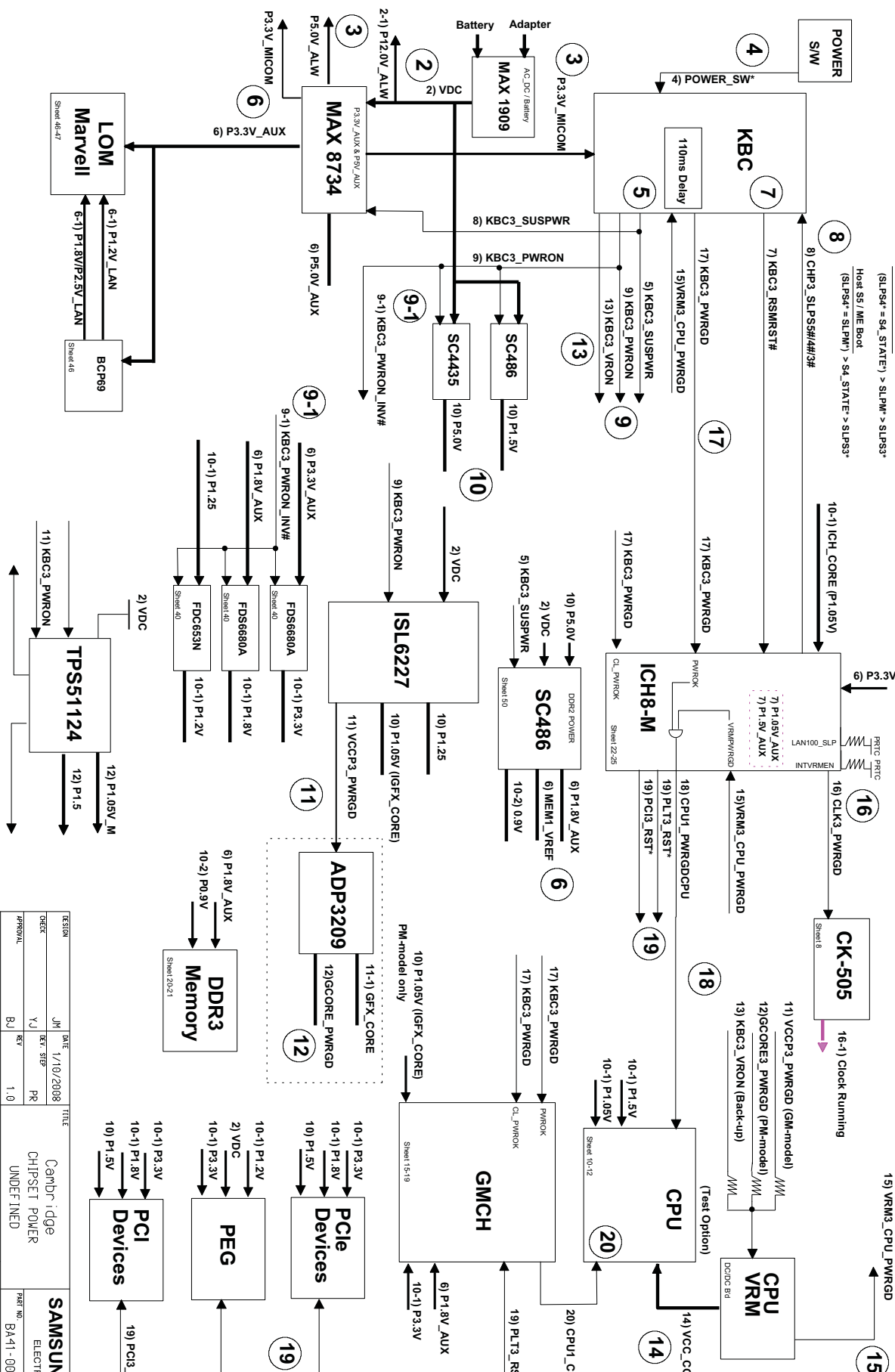
REV. 0.7

Host Boot / ME Off
(SLP#4* = S4_STATE) > (SLPW* = SLP#3*)
M-1) KBC3_DDR_PWRON(TBD) = 8) KBC3_SUSPWR
M-2) KBC3_ME_PWRON = 19) KBC3_PWRON
Host / ME Boot
(SLP#4* = S4_STATE) > SLPW* > SLP#3*
(SLP#4* = SLPW*) > S4_STATE > SLP#3*

RTC
Battery
PRTC_BAT
CHP3_RTICRST#

POWER SEQUENCE

REV. 0.7



REVISION	DATE	TITLE
0001	1/10/2008	Cambridge Chipset Power
0002	1/10/2008	Chipset Power
0003	1/10/2008	Chipset Power
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0097	1/10/2008	Chipset Power
0098	1/10/2008	Chipset Power
0099	1/10/2008	Chipset Power
0100	1/10/2008	Chipset Power

